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Catalytic Embeddings: From Gate Teleportation to Circuit Synthesis

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Abstract

Leading proposals for fault-tolerant quantum computing rely on the implementation of some finite set of encoded gates – often Clifford+T – using a combination of unitary circuits and gate teleportation via distilled resource states. Gates which admit an exact representation over this finite set are synthesized into unitary circuits, while gates which do not admit such a representation are implemented through gate approximation. While asymptotically optimal methods for both over Clifford+T exist, gate approximation remains a significant cost center in fault-tolerant quantum computation.

We introduce an alternative to gate approximation, called catalytic embedding. In this framework, gates which cannot be exactly represented over the encoded fault-tolerant gates are applied through interaction with qubits in fixed, re-usable catalyst states. This allows circuits to be made arbitrarily precise with only constant overhead with respect to circuit volume, using either gate approximation or distillation to prepare the catalyst states. As not every unitary may admit a catalytic embedding over the target gate set, we give explicit conditions and systematic constructions for catalytic embeddings, notably for gates whose entries come from algebraic number rings. This includes all diagonal gates in the Clifford hierarchy, and more generally diagonal gates whose eigenvalues are constructible numbers.

Meeting Information

<https://qmu.zoom.us/j/93426209769?pwd=TjNmaWpvMIYxRzZGUkNzeHdPV2g3QT09>

About the Seminar Series

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